Amendments to the Specification:

Please replace paragraph [0013] with the following amended paragraph:

[0013] FIG. 1 illustrates an embodiment of a storage array network system of the present invention. The primary storage array controller module 70 mini hub 1 is connected to port 4 of the storage device array switch 90, such as a Fibre Channel quick loop switch. As shown in FIG. 2, the storage array device array switch 90 receives eight inputs. The eight inputs represent the four combinations possible between the first (i.e., top) array controller units 72, 82 and the second (i.e., bottom) array controller units 74, 84. That is, a first combination is the pair of the top array controller unit 72 and bottom array controller unit 74 of the primary array controller module, a second combination of the top array controller unit 72 of the primary array controller module and the bottom controller 84 of the secondary array controller module, a third combination of the top array controller unit 82 of the secondary array controller module and the bottom array controller unit 74 of the primary array controller module, and a fourth combination of the top array controller unit 82 and the bottom array controller unit 84 of the secondary array controller module. Port 9 of the storage device array switch 90 is connected to port (b) of the Environmental Service Module 102 of the first tray 100. Port (a) of Environmental Service Module 102 of the first tray 100 is connected to port (b) of Environmental Service Module 112. Port (a) of the Environmental Service Module 106 is connected to port 10 of the storage device array switch 90. Port 8 of the storage device array switch 90 is connected to mini hub 1 of the secondary array controller module 80. These connections establish one loop with two redundant array controller module access to the bank of the drive trays. In a similar way, other loops may be implemented using other controllers. The mini hub connections provide the capability to practice the method of the present invention in which redundant paths/loops are provided to the bank of drive trays for two or more redundant array controller modules.